

List of reference symbols

	1	Semiconductor substrate
	2	Shallow trench isolation
5	3	Gate dielectric
	4	Gate layer
	5	Hard mask layer
	6	Gate sidewall insulation layer
	7, 7A	Spacer
10	8	Insulation mask layer
	8A	Depression sidewall insulation layer
	9	Depression bottom insulation layer
	10	Seed layer
	11	Seed protection layer
15	12	Seed mask layer
	13	Growth layer
	14	Implantation spacer
	S	Source region
	D	Drain region
20	G	Gate stack
	F	Filling layer
	VI	Depression insulation layer
	SV	Source depression
	DV	Drain depression
25	I	Implantation